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(54) **Duplexer.**

(57) A duplexer usable for both transmission and reception in a mobile communication apparatus such as a car phone has both its transmission section and reception section mounted on a single substrate. The transmission section is connected between an input terminal and an antenna terminal and includes an isolator and either a circuit having only inductors and capacitors or a trap circuit having a dielectric resonator and an inductor and a Π -type low-pass filter having an inductor and capacitors. The reception section is comprised of a filter using dielectric resonators.

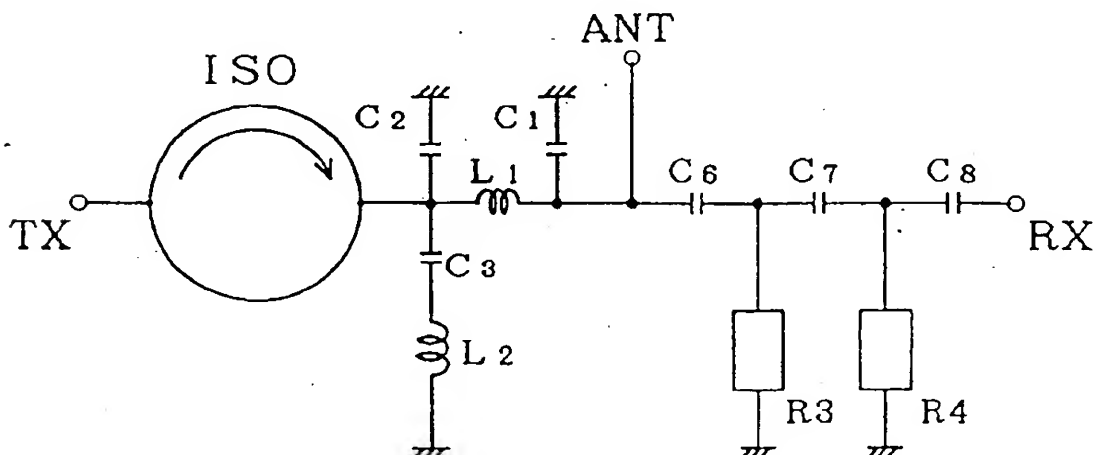


Fig. 1

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Background of the Invention

This invention relates to a duplexer, usable in a mobile communication apparatus such as a car phone and a portable phone, for allowing a single antenna to be used for both transmission and reception of signals.

There is a trend for compactness and high-density mounting in mobile communication apparatus such as portable telephones, and the progress is in the direction of digital systems. Under these circumstances, there are increased demands for compactness and high-density mounting for the components which are used in such apparatus.

An antenna circuit for an apparatus such as a portable telephone is generally structured, as shown in Fig. 4, by connecting an isolator ISO to a duplexer, the duplexer being composed of a transmission filter and a reception filter and the isolator being connected to the input terminal of its transmission section. Fig. 5 shows a circuit diagram of a duplexer for a prior art mobile communication apparatus such as a portable analog telephone using dielectric resonators. Its transmission section comprises a band elimination filter using two dielectric resonators R_1 and R_2 as well as capacitors $C_1 - C_5$ and inductors L_1 and L_3 , and its reception section comprises a two-stage bandpass filter using two dielectric resonators R_3 and R_4 as well as capacitors $C_6 - C_8$. An antenna terminal ANT in the middle is connected to an antenna. The inductor L_1 and the capacitors C_1 and C_2 together form a II-type low-pass filter adapted to adjust the phase at the antenna terminal ANT of the band elimination filter such that the reflection phase becomes open in the pass band of the bandpass filter in the reception section. The dielectric resonators $R_1 - R_4$, capacitors $C_1 - C_8$ and inductors $L_1 - L_3$ are mounted on a single substrate to form a duplexer of a unified structure and, when it is used in an apparatus such as a portable telephone, such a duplexer and an isolator are mounted separately on a circuit board.

Thus, a prior art duplexer has a transmission section having a filtering function with large attenuation in the pass band of the reception section, and use is therefore made of a dielectric filter with large attenuation comprising a plurality of dielectric resonators. Thus, many dielectric resonators, capacitance-providing elements and inductance-providing elements such as coils were required. This makes it difficult to provide a compact duplexer, and since these many elements must be properly arranged and soldered, not only the cost of components but the cost of production is increased. Moreover, such a prior art duplexer has the problem of a large insertion loss.

Summary of the Invention

It is therefore an object of this invention to elim-

inate the problems as described above of prior art duplexers and to provide an improved duplexer with a small loss, which is composed of an isolator and a fewer components, can be produced at a reduced cost and in a compact form, can be mounted on a circuit board easily and requires a reduced area for mounting.

A duplexer embodying this invention, with which the above and other objects can be accomplished, may be characterized as having its transmission section formed with an isolator and a circuit having only inductors and capacitors, and its reception section comprised of a filter using dielectric resonators, both the transmission and reception sections, inclusive of the isolator, being mounted on a single substrate to form a unitary structure. The circuit having only inductors and capacitors in the transmission section is formed as a combination of a series-connected resonance circuit with an inductor and a capacitor and a II-type low-pass filter having an inductor and capacitors.

The transmission section may alternatively be comprised of an isolator, a trap circuit including a single dielectric resonator and a capacitor, and a II-type low-pass filter having an inductor and capacitors.

With a duplexer thus formed, fewer components are required because its transmission section includes no dielectric resonators or only one dielectric resonator, and a compact duplexer with a low loss can be realized.

Brief Description of the Drawings

The accompanying drawings, which are incorporated in and form a part of this specification, illustrate embodiments of the invention and, together with the description, serve to explain the principles of the invention. In the drawings:

Fig. 1 is a circuit diagram of a duplexer embodying this invention;

Fig. 2 is a diagonal external view of the duplexer of Fig. 1 mounted on a substrate;

Fig. 3 is a circuit diagram of another duplexer according to another embodiment of the invention;

Fig. 4 is a schematic diagram showing the general structure of a duplexer; and

Fig. 5 is a circuit diagram of a prior art duplexer.

Throughout herein, components which are equivalent although belonging to different duplexers are indicated by the same symbols for convenience.

Detailed Description of the Invention

As shown in Fig. 1, a duplexer according to this invention has a bandpass filter in its reception section formed with capacitors C_7 , C_8 and C_6 inserted respectively between two dielectric resonators R_3 and R_4 , between the dielectric resonator R_4 and an output ter-

minal RX, and between the dielectric resonator R_3 and an antenna terminal ANT. In its transmission section, an isolator ISO is connected to an input terminal TX, and a II-type low-pass filter comprised of an inductor L_1 and capacitors C_1 and C_2 and a series-connected trap circuit comprising an inductor L_2 and a capacitor C_3 are connected between the isolator ISO and the antenna terminal ANT.

The main function of this II-type low-pass filter comprised of the inductor L_1 and capacitors C_1 and C_2 and the series-connected trap circuit comprising the inductor L_2 and the capacitor C_3 is to adjust the phase such that the reflection phase in the passband of the band pass filter at a receiving station becomes open at the antenna terminal ANT. In other words, the circuit in the transmission section described above is for the purpose of matching the isolator with the reception filter, and not for obtaining attenuation inside the passband, at the receiving station.

In summary, the circuit according to this embodiment of the invention is obtained by replacing the prior art filter circuit using dielectric resonators (as shown in Fig. 5) by a circuit having only inductors and capacitors, and this has become possible because attenuation inside the passband at the receiving station is unnecessary in the case of a duplexer comprising a transmission filter and a reception filter used for a digital portable telephone, etc.

A duplexer as shown in Fig. 1 is formed according to this invention by mounting the isolator ISO, the dielectric resonators R_3 and R_4 , inductors L_1 and L_2 and capacitors $C_1 - C_3$ and $C_6 - C_8$ on a single substrate, as shown in Fig. 2. Grounding electrodes GND are formed as wiring patterns both on the upper and lower surfaces of the substrate (although the bottom surface is not separately illustrated). Similarly, although not shown in Fig. 2, terminals for connections, as well as input and output electrodes are formed also as wiring patterns on the upper and lower surfaces of the substrate.

In summary, a duplexer according to this embodiment of the invention is characterized as having a simplified circuit in the transmission section and being a unified structure including an isolator. Since the transmission section can thus be formed without using a dielectric resonator and an isolator is included as a part of the unified structure, not only dielectric resonators but also inductors and capacitors which would be required to be connected to such dielectric resonators to form a filter are no longer required. As a result, the total number of components to be assembled (and soldered) is reduced and the insertion loss can also be reduced according to this invention.

Fig. 3 shows another duplexer embodying this invention characterized as having its transmission section formed by connecting an isolator ISO to an input terminal TX and connecting a II-type low-pass filter comprised of an inductor L_1 and capacitors C_1 and C_2

and a series-connected trap circuit comprising a dielectric resonator R_2 and a capacitor C_3 between the isolator ISO and the antenna terminal ANT. Its reception section is structured as explained above with reference to Fig. 1. In other words, the duplexer shown in Fig. 3 may be described as using the dielectric resonator R_2 in the place of the inductor L_2 of Fig. 1. Thus, the circuit for the transmission section composed of the inductor L_1 , capacitors C_1 , C_2 and C_3 and the dielectric resonator R_2 is for the purpose of matching the isolator in the transmission section with the receiving circuit at a receiving station.

As explained above, a duplexer according to the second embodiment of the invention is characterized wherein its transmission section does not have the filtering function obtainable with a plurality of dielectric resonators which were necessary in prior art duplexer for obtaining attenuation. Instead, the transmission section includes only one dielectric resonator, and it is for the purpose of matching. Thus, the transmission section is much simplified as compared to prior art duplexers.

Although not separately illustrated, a duplexer according to this invention, of which the circuit diagram is shown in Fig. 3, is characterized in that all these components shown in Fig. 3, inclusive of the isolator, are mounted on a single substrate, as shown in Fig. 2. Since use is made of a single substrate and the reception section includes only one dielectric resonator for matching, the total number of components, as well as the insertion loss, can be reduced also according to this embodiment of this invention.

Although this invention has been described above in terms of only a limited number of examples, the invention is not intended to be limited by these illustrated examples. Many modifications and variations are possible within the scope of the invention. For example, although the illustrated examples included a two-stage resonator apparatus for the reception section, the circuit for the reception section may be formed with a single-stage dielectric resonator or a multi-stage resonator apparatus with three or more stages. The manner of connecting the resonators is not intended to limit the scope of the invention. As another example, although a plurality of dielectric resonators, each comprising a dielectric block having a throughhole serving as a resonator, are used as a filter for the reception section in the illustrated examples, use may equally well be made of a dielectric resonator apparatus (or dielectric filter) comprising a single dielectric block having a plurality of throughholes serving as resonators. In summary, all such modifications and variations that are obvious to a person skilled in the art are intended to be within the scope of the invention. Duplexers according to this invention are finally characterized as having a simplified circuit for its transmission section and every component inclusive of an isolator mounted on a single substrate

such that they can be easily mounted on a circuit board for an apparatus such as a portable telephone and the cost of the mounting can be reduced. Since the transmission section can be formed without using any dielectric resonator or only one dielectric resonator, furthermore, the total number of the components can be reduced significantly. As a result, the insertion loss is reduced, the duplexer can be made compact and the cost of parts as well as the overall production cost can be significantly reduced.

Claims

1. A duplexer comprising:
 - a transmission section comprising an isolator and a transmission circuit comprising inductors and capacitors;
 - a reception section comprising a reception filter having a dielectric resonator apparatus; and
 - a single substrate on which said transmission and reception sections are mounted.
2. The duplexer according to claim 1 wherein said transmission section consists of said isolator and a transmission circuit consisting of inductors and capacitors, wherein said reception section consists of said reception filter, and wherein said reception filter consists of dielectric resonators and capacitors.
3. The duplexer according to claim 1 or claim 2 further comprising an antenna terminal, an input terminal and an output terminal, said transmission section being connected between said antenna terminal and said input terminal, said reception section being connected between said antenna terminal and said output terminal.
4. The duplexer according to any one of claims 1 to 3 wherein said transmission circuit comprises a II-type low-pass filter having an inductor and capacitors and a series-connected resonance circuit having an inductor and a capacitor.
5. The duplexer according to any one of claims 1 to 3 wherein said transmission circuit consists of a II-type low-pass filter having an inductor and capacitors and a series-connected resonance circuit having an inductor and a capacitor.
6. The duplexer according to claim 1 further comprising grounding electrodes formed on surfaces of said single substrate.
7. A duplexer comprising:
 - a transmission section comprising an isolator, a trap circuit having a dielectric resonator

and an inductor, and a II-type low-pass filter having an inductor and capacitors;

a reception section comprising a reception filter having a dielectric resonator apparatus; and a single substrate on which said transmission and reception sections are mounted.

8. The duplexer according to claim 1 wherein said transmission section consists of said isolator, said trap circuit and said II-type low-pass filter, wherein said reception section consists of said reception filter, and wherein said reception filter consists of dielectric resonators and capacitors.
9. The duplexer according to claim 7 or claim 8 further comprising an antenna terminal, an input terminal and an output terminal, said transmission section being connected between said antenna terminal and said input terminal, said reception section being connected between said antenna terminal and said output terminal.
10. The duplexer according to claim 7 further comprising grounding electrodes formed on surfaces of said single substrate.

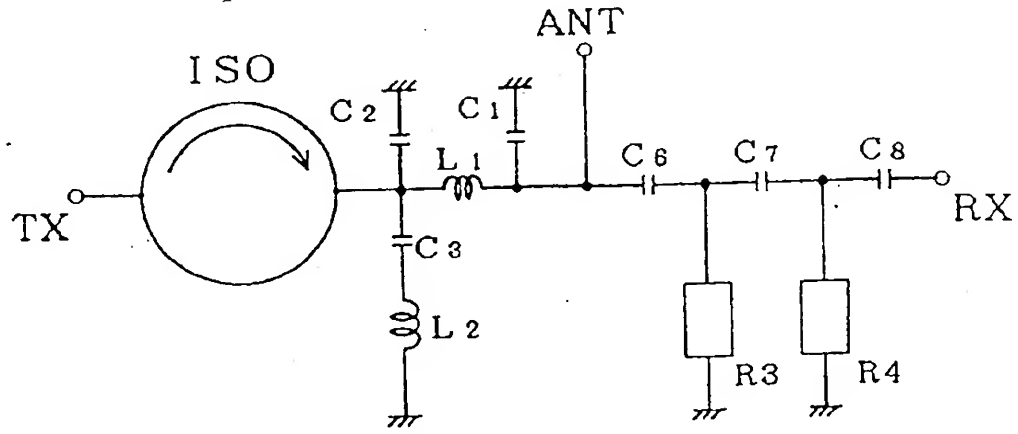


Fig. 1

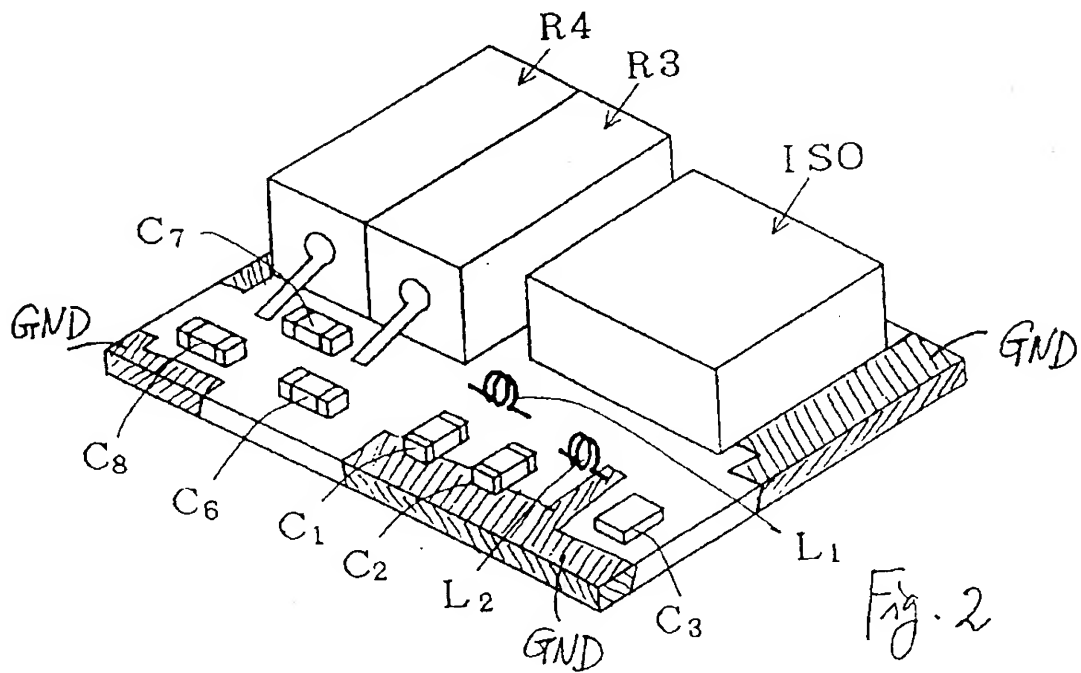


Fig. 2

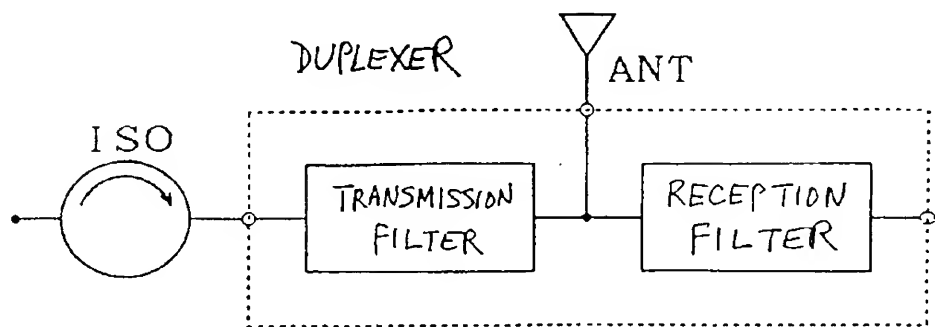
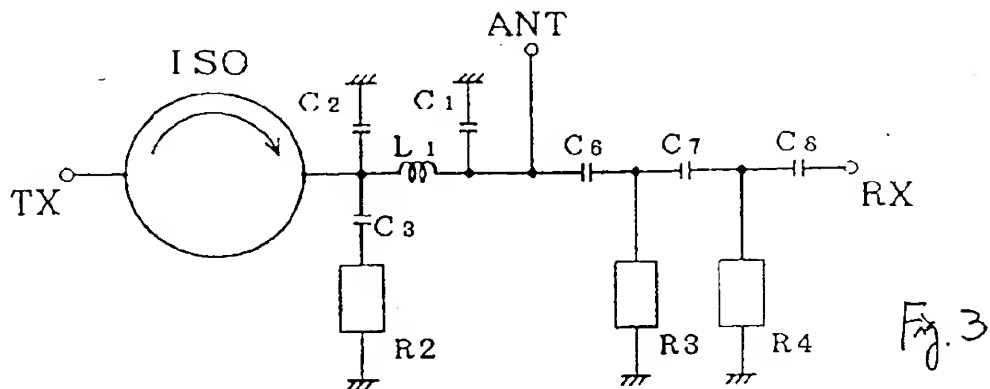
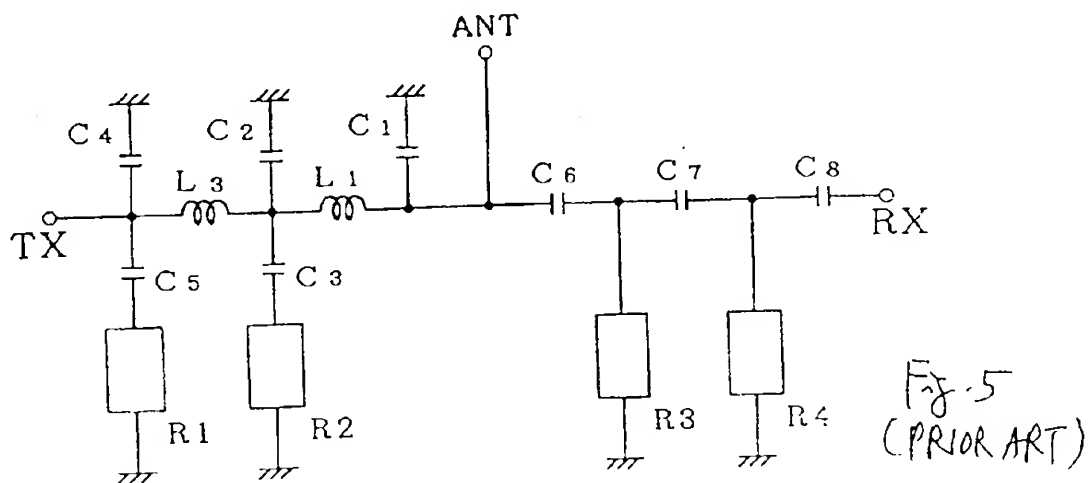


Fig. 4 (PRIOR ART)





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EUROPEAN SEARCH REPORT

Application Number
EP 95 30 2705

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl. 6)
A	PATENT ABSTRACTS OF JAPAN vol. 4 no. 189 (E-39) [671] ,25 December 1980 & JP-A-55 132103 (SHIMADA RIKI KOGYO K.K.) 14 October 1980, * abstract *	1	H01P1/213
A	US-A-5 023 866 (DEMURO) * column 4, line 31 - column 5, line 22; figure 5 *	1,6,7,10	
A	PATENT ABSTRACTS OF JAPAN vol. 17 no. 128 (E-1333) ,18 March 1993 & JP-A-04 304003 (MURATA MFG CO LTD) 27 October 1992, * abstract *	1,3,7	
A	PATENT ABSTRACTS OF JAPAN vol. 15 no. 219 (E-1074) ,5 June 1991 & JP-A-03 062626 (NEC CORP) 18 March 1991, * abstract *	1	
			TECHNICAL FIELDS SEARCHED (Int. Cl. 6)
A	PATENT ABSTRACTS OF JAPAN vol. 11 no. 51 (E-480) [2498] ,17 February 1987 & JP-A-61 214625 (TOKYO ELECTRIC CO LTD) 24 September 1986, * abstract *	4,5	H01P H03H H04B
P,X	GB-A-2 282 029 (FUJITSU LTD) * the whole document *	1,3	
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 11 July 1995	Examiner Den Otter, A
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